

1 At line 17 of claim 1 (amended), i.e. at line 22 of page 2
2 of Amendment A, between the word "electrically" and the word
3 "connected", please insert the word --directly--.

4
5 REMARKS

6
7 Reconsideration and reexamination is hereby requested.

8
9 The examiner rejected claim 1 (amended) under 35 U.S.C.
10 103(a) as being unpatentable over Temple et al. (US 4,905,095)
11 because figure 5 of Temple depicts a metallization layer 70 that
12 is electrically connected to plate 60a via device 12b. The
13 specification, at column 11, line 49 describes device 12b as being
14 a field effect transistor with its "drain" electrode connected to
15 layer 70 [Column 2, line 54]. The specification describes an
16 instance of such a device as having anode, cathode and gate
17 electrodes [column 6, lines 39-41]. As a consequence, the
18 configuration in figure 5 of Temple anticipates or makes obvious
19 the present invention only if one considers the internal elements
20 of the field effect transistor, 12b, to constitute an electrical
21 connection between plate 60a and metallization layer 70.

22
23 In the present application, however, the specification
24 describes, and the drawings depict, a package in which each lower
25 surface signal connector pad on the lid is electrically directly
26 connected to one of the signal connector pads located upon the
27 upper surface of the die. The specification and the drawings do
28 not depict any intervening devices that provide some type of